



2 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

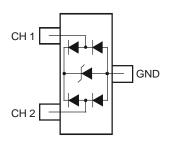
Features

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 2 Channels of ESD protection
- Low Channel Input Capacitance
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.009 grams (Approximate)





Device Schematic

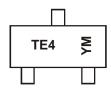
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|----------------|-------|-------------------|
| D1213A-02SOL-7 | SOT23 | 3,000/Tape & Reel |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



TE4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

Date Code Key

| Date Code Ney | - | | | - | | | | | | | | |
|---------------|-----|-----|------|-----|------|-----|-----|------|-----|------|-----|------|
| Year | 201 | 1 | 2012 | | 2013 | 20 | 14 | 2015 | | 2016 | | 2017 |
| Code | Y | | Z | | Α | | В | С | | D | | E |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



Maximum Ratings @TA = 25°C unless otherwise specified

| Characteristic | Symbol | Value | Unit | Conditions |
|------------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Current (Note 7) | I _{PP} | 5 | Α | 8/20 μs, Per Fig. 2 |
| ESD Protection – Contact Discharge | V _{ESD_Contact} | ±8 | kV | Standard IEC 61000-4-2 |
| ESD Protection – Air Discharge | V_{ESD_Air} | ±15 | kV | Standard IEC 61000-4-2 |

Thermal Characteristics

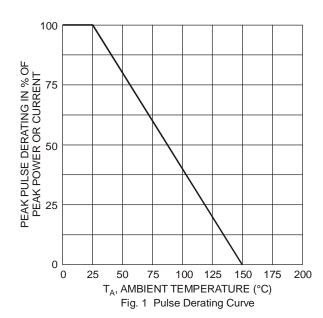
| Characteristic | Symbol | Value | Unit |
|--|------------------|-------------|------|
| Package Power Dissipation (Note 5) | P_{D} | 300 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{	heta JA}$ | 417 | °C/W |
| Operating Temperature Range | TJ | -55 to +125 | °C |
| Storage Temperature Range | T _{STG} | -65 to +150 | °C |

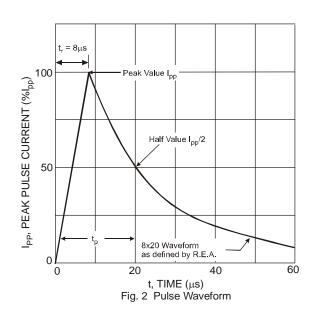
Electrical Characteristics @T_A = 25°C unless otherwise specified

| Characteristic (Note 7) | Symbol | Min | Тур | Max | Unit | Test Conditions |
|---|------------------|-----|------|------|------|-------------------------------------|
| Reverse working voltage | VRWM | - | - | 3.3 | V | - |
| Reverse current (Note 6) | I _R | - | 0.1 | 1.0 | μΑ | $V_R = V_{RWM} = 3.3V$ |
| Reverse breakdown voltage | V _{BR} | 6.0 | 7.5 | 9.0 | V | I _R = 1mA |
| Forward voltage | VF | 0.6 | 0.8 | 0.95 | V | $I_F = 8mA$ |
| Reverse clamping voltage, Positive Transients | V_{CL1} | - | 10.0 | - | V | $I_{PP} = 1A, t_p = 8/20 \mu s$ |
| Reverse clamping voltage, Negative Transients | V _{CL2} | - | -1.7 | - | V | $I_{PP} = -1A$, $t_p = 8/20 \mu s$ |
| Dynamic resistance | R_{DYN} | - | 0.9 | - | Ω | $I_R = 1A, t_p = 8/20 \mu s$ |
| Capacitance | C _T | = | 0.85 | 1.2 | pF | V _R = 1.65V, f = 1MHz |

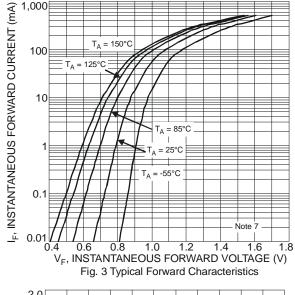
Notes:

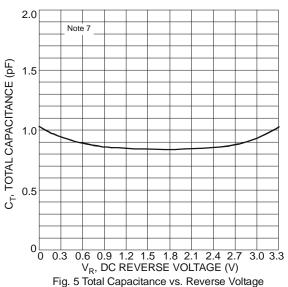
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Measured between any channel and GND

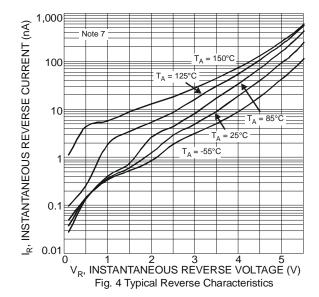




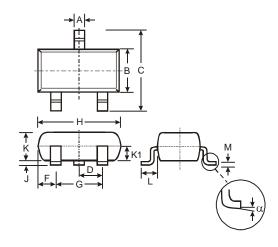








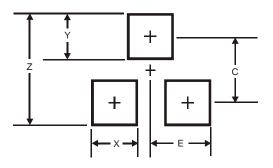
Package Outline Dimensions



| SOT23 | | | | | | | |
|-------|----------------------|------|-------|--|--|--|--|
| Dim | Min | Max | Тур | | | | |
| Α | 0.37 | 0.51 | 0.40 | | | | |
| В | 1.20 | 1.40 | 1.30 | | | | |
| С | 2.30 | 2.50 | 2.40 | | | | |
| D | 0.89 | 1.03 | 0.915 | | | | |
| F | 0.45 | 0.60 | 0.535 | | | | |
| G | 1.78 | 2.05 | 1.83 | | | | |
| Н | 2.80 | 3.00 | 2.90 | | | | |
| J | 0.013 | 0.10 | 0.05 | | | | |
| K | 0.903 | 1.10 | 1.00 | | | | |
| K1 | - | 1 | 0.400 | | | | |
| L | 0.45 | 0.61 | 0.55 | | | | |
| M | 0.085 | 0.18 | 0.11 | | | | |
| α | 0° | 8° | - | | | | |
| All | All Dimensions in mm | | | | | | |



Suggested Pad Layout



| Dimensions | Value (in mm) |
|------------|---------------|
| Z | 2.9 |
| Х | 0.8 |
| Υ | 0.9 |
| С | 2.0 |
| E | 1.35 |

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